RAVINDRA R. KURAMKOTE
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VISA Status: Permanent resident

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EDUCATION

Ph.D., Computer Science, University of Utah, Expected Graduation: Spring 2002.
M.S., Computer Science, Arizona State University, 1992.
B.E., Electronics and Communication, University of Mysore, 1984.

EXPERIENCE

August 2000-Present, Software Design Engineer, Enterprise Systems Technology Lab, Hewlett-Packard.

September 1998-July 2000, Research Associate/Assistant, Impulse Project, University of Utah. Evaluation of the architectural simulators and consulting on the design of the memory controller.

October 1994-August 1998, Research Associate, Avalanche Project, University of Utah. Designed, verified, and simulated a directory based shared memory controller.

June 1994-September 1994, Research intern, VLIW Architecture Group, Hewlett-Packard Laboratories. Simulated and tested HP's VLIW processor architecture, which was the precursor to the Intel Merced.

January 1993-May 1994, Research Assistant, Mach Shared Object Project, University of Utah. Implemented mechanisms to add flexibility at run time to the Mach operating system. Developed a transaction manager server to handle distributed transactions, an object server to manage small persistent objects, and a name server using the NFS XDR protocol interface on the UNIX platform.

September 1992-December 1992, Teaching Assistant, Department of Computer Science, University of Utah. Assisted in projects and graded homework assignments for a graduate course in Operating System.

January 1990-August 1992, Graduate Assistant, Graduate College, Arizona State University. Developed and maintained database software using FOCUS.

August 1989-December 1989, Computer Laboratory Assistant, Department of Computer Science, Arizona State University. Graded homework for an undergraduate course in Computer Literacy.

September 1985-June 1989, Field Engineer, DCM Data Products, India. Maintained hardware and developed diagnostic software for mini/micro computers.

August 1984-August 1985, Computer Engineer, Indian Express Pvt. Ltd., India. Developed and maintained hardware/software at the computer division.

COMPUTER SKILLS

Simulation and performance analysis of computer hardware systems.

Languages and tools such as C, C++, Perl, Make, and assembly languages(PA-RISC, Sparc, and X86).

Internals of operating systems such as BSD 4.4 and Mach.

Cache and bus protocols of modern processors (e.g., HP PA-7200/PA-8000 and MIPS R10000).

MISCELLANEOUS

Invited Talk: "Distributed Shared Memory in Avalanche" at Hewlett-Packard Laboratories, Palo Alto, CA.

Awards: Merit Scholarship from the Government of India, Academic Scholarship from the Arizona State University for academic excellence, Certificate from Upsilon Pi Epsilon, Arizona State University.